

09/926364

214682US

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
 TAKEO SUZUKI ET AL : ATTN: APPLICATION DIVISION
 SERIAL NO: NEW U.S. PCT APPLN :
 (Based on PCT/JP01/01262)
 FILED: HEREWITH :
 FOR: RECEIVER :

PRELIMINARY AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
 WASHINGTON, D.C. 20231

SIR:

Prior to a first examination on the merits, please amend the above-identified
 application as follows:

IN THE SPECIFICATION

Page 3, please insert the following heading between lines 11 and 12:

SUMMARY OF THE INVENTION

Page 9, please insert the following heading between lines 2 and 3:

DESCRIPTION OF THE PREFERRED EMBODIMENTS

09/926364-10201

IN THE CLAIMS

Please amend Claims 7-10 as follows:

7. (Amended) The receiver according to claim 6, wherein the quantizing error reduction signal generated by the reduction signal generator is a triangular wave signal.

8. (Amended) The receiver according to claim 6, wherein the quantizing error reduction signal generated by the reduction signal generator is a signal having an orthogonal code having an orthogonal relation with a spreading code used when the de-spread unit de-spreads the input digital signal.

9. (Amended) The receiver according to claim 6, wherein the quantizing error reduction signal generated by the reduction signal generator is a signal having a high frequency band as compared with a frequency band of the receiving analog signal.

10. (Amended) The receiver according to claim 6, wherein the quantizing error reduction signal generated by the reduction signal generator is a rectangular wave signal having a uniform distribution.

IN THE ABSTRACT OF THE DISCLOSURE

Please replace the Abstract with the following new Abstract:

ABSTRACT OF THE DISCLOSURE

A receiver including A/D converters for converting received analog signals into digital signals. A quantizing error reduction signal generator generates a random noise quantizing error reduction. Adders add the converted digital signal and the quantizing error reduction signal. Bit shift circuits reduce a bit number of addition. Low-pass filters remove a

quantizing error reduction signal included in the digital signal having the reduced bit number.

Also provided are matched filters and a demodulation section.

REMARKS

Favorable consideration of this application, as presently amended, is respectfully requested.

The present preliminary amendment is submitted to place the above-identified application in more proper format under United States practice.

By the present preliminary amendment, the specification has been amended to include suggested headings.

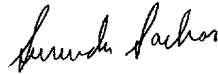
Claims 7-10 have been amended by the present response to correct for minor informalities in each of their dependencies.

A new Abstract, believed to be in more proper format under United States practice, is also submitted herein.

The present application is believed to be in condition for a full and thorough examination on the merits. An early and favorable consideration of the present application is hereby respectfully requested.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



Gregory J. Maier
Attorney of Record
Registration No. 25,599
Surinder Sachar
Registration No. 34,423



22850

(703) 413-3000
Fax No.: (703)413-2220
GJM/SNS:kst
I:\atty\SNS\214682us-pr.wpd

Marked-Up Copy

Serial No: _____

Amendment Filed on: _____

IN THE SPECIFICATION

Page 3, please insert the following heading between lines 11 and 12:

--SUMMARY OF THE INVENTION--

Page 9, please insert the following heading between lines 2 and 3:

--DESCRIPTION OF THE PREFERRED EMBODIMENTS--

IN THE CLAIMS

Please amend Claims 7-10 as follows:

--7. (Amended) The receiver according to claim [2] 6, wherein the quantizing error reduction signal generated by the reduction signal generator is a triangular wave signal.

8. (Amended) The receiver according to claim [2] 6, wherein the quantizing error reduction signal generated by the reduction signal generator is a signal having an orthogonal code having an orthogonal relation with a spreading code used when the de-spread unit de-spends the input digital signal.

9. (Amended) The receiver according to claim [2] 6, wherein the quantizing error reduction signal generated by the reduction signal generator is a signal having a high frequency band as compared with a frequency band of the receiving analog signal.

10. (Amended) The receiver according to claim [2] 6, wherein the quantizing error reduction signal generated by the reduction signal generator is a rectangular wave signal having a uniform distribution.--

IN THE ABSTRACT OF THE DISCLOSURE

Abstract (New).